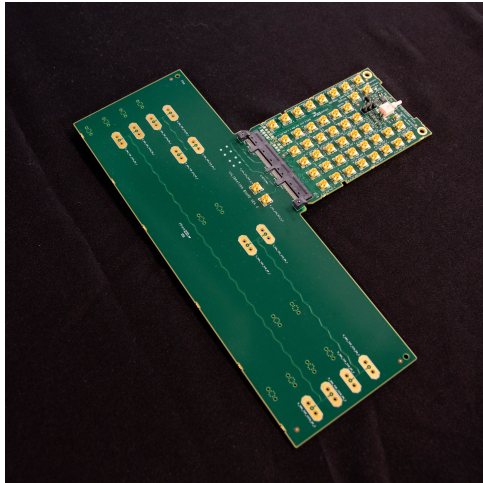


## OCP 3.0 SI Gen 3 CLB Test Board (Lanes 0-7)

Signal Integrity Board for OCP 3.0 NIC Physical Layer Conformance



### Category

InterOperability Lab/OCP NIC &  
PCIe Test Fixtures

[View online](#)



Signal Integrity Board for OCP 3.0 SI Transmitter Gen 3 (8GT/s) used for testing Root Complex Transmitters and Receiver Calibration (paired with the CBB) of AIC Receivers. Additionally these fixtures can be used in the same fashion for validating PCIe SSDs using the EDSFF form factor for E1.L, E1.S, and E3.

Comes with one (1) CLB for lanes 0-7 and one (1) calibration board for 1x thru.

Please contact us for the current lead time.

**Note:** Please use English characters when placing orders.

**NO WARRANTY:** This hardware is provided **"AS IS"**