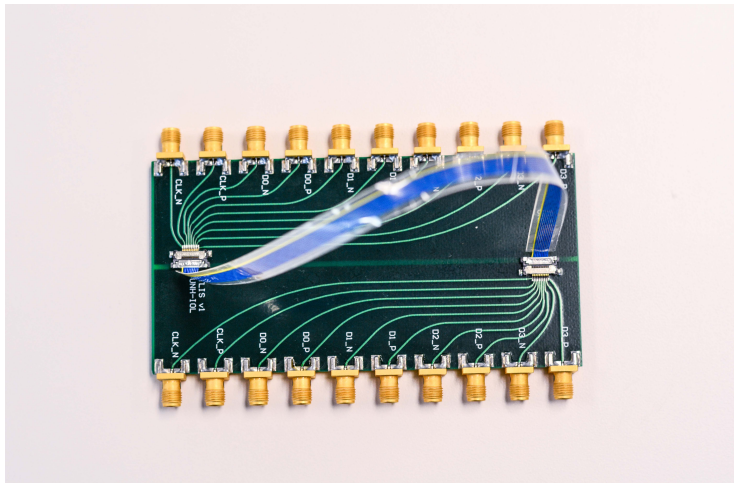


MIPI TLIS Board

The MIPI D-PHY TLIS Board is a reference test channel designed to match the Transmission Line Interconnect Structure defined in the MIPI D-PHY specification.



The MIPI D-PHY TLIS Board is a reference test channel designed to match the Transmission Line Interconnect Structure defined in the MIPI D-PHY specification. It can be used as a worst-case test channel during MIPI interop testing.

The current lead time for this product is about 15 weeks.

Note: Please use English characters when placing orders.

NO WARRANTY: This hardware is provided **"AS IS"**

Category

InterOperability Lab/MIPI Test
Fixtures

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