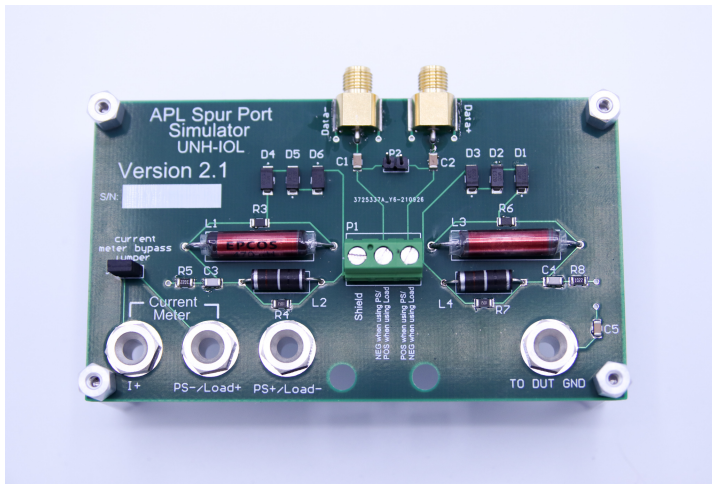


UNH-IOL APL Spur Simulator Board

V2.2



Category

InterOperability Lab/APL Test
Fixtures

Authors

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Mike Goding

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Printed Circuit Board featuring terminal block connection that provides power and data coupling/decoupling network and interface for test and measurement equipment. This board simulates both Spur Power and Spur Load ports to perform all SP and SL tests in the APL Power Conformance Test Specification.

Note: Please use English characters when placing orders.

NO WARRANTY: This hardware is provided **"AS IS"**